



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application of: Pin-Shyne Chin et al.

Serial No.: 09/785,114

Group Art Unit: 2823

Filed: February 20, 2001

Examiner: Coleman, William D.

Confirmation No.: 4177

For: Low Leakage One Transistor Static
Random Access Memory

I hereby certify that this correspondence is being deposited
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Fee, Commissioner for Patents, P.O. Box 1450, Alexandria,
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July 30, 2004

Date

Cheryl L. Powell

TRANSMITTAL LETTER

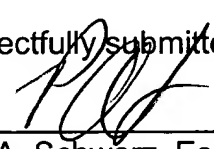
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Dear Sir:

Enclosed herewith is an Associate Power Of Attorney, Change of
Correspondence Address, Issue Fee Transmittal and Acknowledgment Postcard for the
above-captioned U.S. patent application.

Respectfully submitted,

DATE: July 30, 2004


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